

**ABSTRACT OF the DISCLOSURE**

A technique for improved mapping of the contacts of a PLD to the contacts of one or more other electronic components are provided. In one particular exemplary embodiment, the  
5 technique may be realized as a method for mapping contacts of a programmable logic device (PLD) to an electronic component in a signal routing device having one or more layers. The method comprises assigning a set of one or more contacts of the PLD to one or more respective contacts of the electronic  
10 component based at least in part on a pattern of electrically conductive traces routed from respective contacts of the electronic component via one or more channels formed at one or more layers of the signal routing device.